

JAN 03 2007

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**BEST AVAILABLE COPY****REMARKS**

The Notice of Allowance mailed October 27, 2006, has been received and its contents carefully noted. In view of thereof, claims 4 and 14 have been amended in order to correct an inadvertent error introduced in the previous amendment. As previously, claims 1-20 are presently pending in the instant application.

Applicant amended claims 4 and 14 to recite these claims in independent form, incorporating the limitations of independent claims 1 and 11, respectively, from which claims 4 and 14 previously depended. Applicant amended claims 4 and 14 to remove the adverb "directly" from the limitation "a third bus for *directly* connecting the local memory to the access control means." Claims 4 and 14 are directed to a second embodiment of the present invention, as depicted in Figure 2 of the pending application. In this embodiment, the present invention incorporates a selector for selectively connecting the third bus to the local memory. The selector is positioned on the third bus between the local memory and the access control means. As such, the third bus does not directly connect the local memory to the access control means when the selector is provided, as recited in claims 4 and 14.

Applicant regrets this previous oversight but respectfully submits that claims 4 and 14, as well as claims 5, 6, 8, and 10, which depend upon claim 4, and claims 15, 16, 18, and 20, which depend upon claim 14, remain in condition for allowance because none of the cited references disclose nor suggest that which is presently set forth by Applicant's claimed invention as outlined in detail in Applicant's response filed August 14, 2006.

Particularly, Applicant's claimed invention encompasses essentially two embodiments wherein each embodiment includes a local memory. In the first embodiment illustrated in Figure 1, the local memory has two ports with the local memory being directly connected to a central processing unit by a second bus. Therefore, it is possible to transmit data between the

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local memory and the central processing unit at very high speeds. The high speed data transmission takes place independently of other circuits and can be carried out without being influenced by other circuits. The local memory is also directly connected to an access control means or access controller by a third bus. Therefore, as noted above, it is possible to transmit data between the local memory and the access control means at very high speeds. The high speed data transmission takes place independently of other circuits and can be carried out without being influenced by the other circuits.

Similarly, in a second embodiment illustrated in Figure 2, a local memory, an arbitration means and a selection circuit are set forth. Based on the arbitration results of the arbitration means, the central processing unit is connected to the local memory by way of the selection circuit or the access control means is connected to the local memory by way of the selection circuit. In doing so, the local memory, central processing unit and access control means have independent buses and are connected to the selection circuit independently. Better stated, one-to-one connection is established between the local memory and selection circuit, one-to-one connection is established between the central processing unit and selection circuit and one-to-one connection is established between the access control means and selection circuit. In doing so, the central processing unit can directly access the local memory, and the access control means can directly access the local memory. Applicant respectfully submits that the prior art of record neither discloses nor suggests these features.

Applicant appreciates the Examiner's consideration of this amendment to further the prosecution of this application and to ensure that the claims of the application correctly recite the embodied invention. Applicant respectfully requests that the Examiner enter the above amendment to the prosecution record and provide a supplemental notice of allowance as permitted under MPEP § 714.15.

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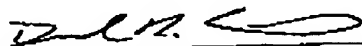
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If the Examiner believes a conference would be of benefit in expediting the prosecution of the instant application, Applicant invites the Examiner to telephone counsel to arrange such a conference.

Respectfully submitted,



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